



COPY

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Lajos Burgyan et al.  
Assignee: Fairchild Semiconductor Corporation  
Title: System and Method for Driving LEDs  
Patent No.: 7,116,086 Issue Date: October 3, 2006  
Application No.: 10/729,729 Filing Date: December 5, 2003  
Examiner: Bao Q. Vu Group Art Unit: 2838  
Docket No.: 24317/82152 Conf. No.: 1311

**Certificate**

Attn: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

MAY 03 2007

**of Correction**

**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION**

Dear Sir:

Please enter the enclosed Certificate of Correction (PTO Form 1050) in the above patent.

The errors sought to be corrected were made by

☐ the Patent and Trademark Office. Thus, no fee is required for the Certificate of Correction pursuant to 37 CFR §1.322.

☒ Applicants (at least in part). See next section for explanation. The appropriate fee under 37 CFR §1.323 has been authorized below.

05/02/2007 MGE BREM1 00000012 501597 7116086

The following errors were the result of Applicant's mistake: 100.00 DA

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Column 5, line 31, "to the charge" should read - -to a charge- -;

Column 5, line 66, "to the charge" should read - -to a charge- -;

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Column 6, line 12, delete "terminal"; and

Column 7, line 2, "single, semiconductor" should read - -single semiconductor- -.

Please correct the following errors made by the USPTO:

Column 5, line 9, "unction" should read - -junction- -.

These mistakes occurred in good faith under the following circumstances:

- ☒ due to clerical and/or typographical error; and
- ☒ are of minor character because they do not materially affect the scope or

meaning of the patent (37 CFR §1.322; M.P.E.P. §1481).

☐ Other: {Enter Description}

Please make the following charges or credits to Deposit Account No. 50-1597:

- ☒ the required fee for correcting Applicant's errors

pursuant to 37 CFR §1.323

\$100.00

Please direct all inquiries concerning this request to the undersigned attorney at (415)

772-1200. This form is submitted in duplicate.

Certificate of Mailing

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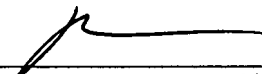
Date

Richard A. Park

Signature

Respectfully submitted,

By:



Philip W. Woo

Attorney of Record

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PWW/rp

April 19, 2007

SIDLEY AUSTIN LLP  
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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,116,086

APPLICATION NO.: 10/729,729

ISSUE DATE: : October 3, 2006

INVENTOR(S) : Lajos Burgyan and Francois Prinz

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, line 9, "unction" should read - -junction- -

Claim 1, line 31, "to the charge" should read - -to a charge- -

Claim 11, line 66, "to the charge" should read - -to a charge- -

Claim 14, line 12, delete "terminal"

Claim 25, line 2, "single, semiconductor" should read - -single semiconductor- -

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Sidley Austin LLP

555 California Street, Suite 2000

San Francisco, California 94104

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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